IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	PAATELA ET AL.	Examiner:	Unknown	
Serial No.	09/849,804	Group Art Unit:	2661	
Filed:	05/04/01	Docket No.	1305.1US01	

Title: SYSTEM AND METHOD FOR PROVIDING TRANSFORMATION OF MULTI-PROTOCOL PACKETS IN A DATA STREAM

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on 3/12/02.

David W. Lynch
Name

Signature

Information Disclosure Statement
Under 37 C.F.R.§1.97(b)

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

Applicant(s) respectfully submit(s) the items of information on the enclosed Form 1449 for the attention of the Examiner in the above-identified application.

This statement should be considered because it is filed before the mailing date of the first Office Action on the merits. Accordingly, no fee is due for consideration of the items listed on the enclosed Form 1449.

A copy of each document or other information listed on the enclosed Form 1449 is enclosed in accordance with 37 C.F.R. §1.98(a)(2) and/or a copy of each document is not provided because it was previously cited by or submitted to the

U.S. Patent and Trademark Office in a parent application in accordance with 37 C.F.R. §.1.98(d).

No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§102 and 103. In addition, Applicant(s) do(es) not represent that a reference has been thoroughly reviewed or that any relevance of any portion of a reference is intended, and reserve the right to establish otherwise under 37 C.F.R. §1.131 or others.

Consideration of the items listed is respectfully requested. According to M.P.E.P. §609, Applicant(s) request(s) that the Examiner return a copy of the attached Form 1449, marked as being considered and initialed by the Examiner, to the undersigned with the next official communication.

Authorization is hereby given to charge any additional fees or credit any overpayments that may be deemed necessary to Deposit Account Number 50-1038.

Respectfully submitted,

Altera Law Group, LLC
6500 City West Parkway, Suite 100
Minneapolis, Minnesota 55344-7701
952-253-4100

Date: 3/12/02

By: _____
David W. Lynch
Reg. No. 36,204

DWL:tmj/ems

2661
03-28-02

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CERTIFICATE UNDER 37 C.F.R. 1.8: The undersigned hereby certifies that this Transmittal Letter and the paper, as described herein, are being deposited in the United States Postal Service, as first class mail, with sufficient postage, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on 3/12/02

David W. Lynch
Name

Signature

Assistant Commissioner for Patents
Washington, D.C. 20231

☒ Information Disclosure Statement, Form 1449, copies of forty-three reference(s)
☒ Transmittal Sheet
☒ Return postcard

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Respectfully submitted,

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6500 City West Parkway, Suite 100
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INFORMATION DISCLOSURE STATEMENT

PTO Form 1449

Docket Number	Serial Number
1305.1US01	09/849,804
Applicant(s)	
Paatela et al.	
Filing Date	**Group Art Unit**
05/04/01	2661

U.S. PATENT DOCUMENTS

EXAMINER INITIALS	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE (IF APPROPRIATE)
		6,175,568	01/16/01	Awdeh			
		6,167,445	12/26/00	Gai et al.			
		6,166,403	12/26/00	Castagnetti et al.			
		6,141,686	10/31/00	Jackowski et al.			
		6,136,638	10/24/00	Lee et al.			
		6,072,989	06/06/00	Witters et al.			
		6,047,002	04/04/00	Hartmann et al.			
		6,046,980	04/04/00	Packer			
		6,046,979	04/04/00	Bauman			
		6,032,190	02/29/00	Bremer et al.			
		5,995,439	11/30/99	Watanabe et al.			
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		5,828,654	10/27/98	Takase et al.			
		5,812,476	09/22/98	Segawa			
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		5,666,353	09/09/97	Klausmeier et al.			
		5,600,598	02/04/97	Skjaveland et al.			
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		5,541,920	07/30/96	Angle et al.			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIALS	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION YES	TRANSLATION NO

OTHER DOCUMENTS

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		William Wong, "Network Processors Take The High Road... And The Low Road," Electronic Design, July 10, 2000, http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6798, Retrieved June 15, 2001, 3 pgs.
		"Network Processors Take The High Road... And The Low Road," Electronic Design, July 10, 2000, http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6799 , Retrieved June 15, 2001, 2 pgs.

Examiner:	Date Considered:

INFORMATION DISCLOSURE STATEMENT PTO Form 1449	Docket Number 1305.1US01	Serial Number 09/849,804
	Applicant(s) Paatela et al.	
	Filing Date 05/04/01	Group Art Unit 2661

		"Network Processors Take The High Road... And The Low Road," Electronic Design – July 10, 2000, http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6800, Retrieved March 7, 2002, 3 pgs.
		"Network Processors Take The High Road... And The Low Road," Electronic Design, July 10, 2000, http://www.planetee.com/planetee/servlet/DisplayDocument?ArticleID=6802 , Retrieved June 15, 2001, 1 pg.
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		Husak et al., "Network Processor Programming Models: The Key to Achieving Faster Time-to-Market and Extending Product Life," C-PORT, A Motorola Company, May 4, 2000, 8 pgs.

Examiner:	Date Considered: